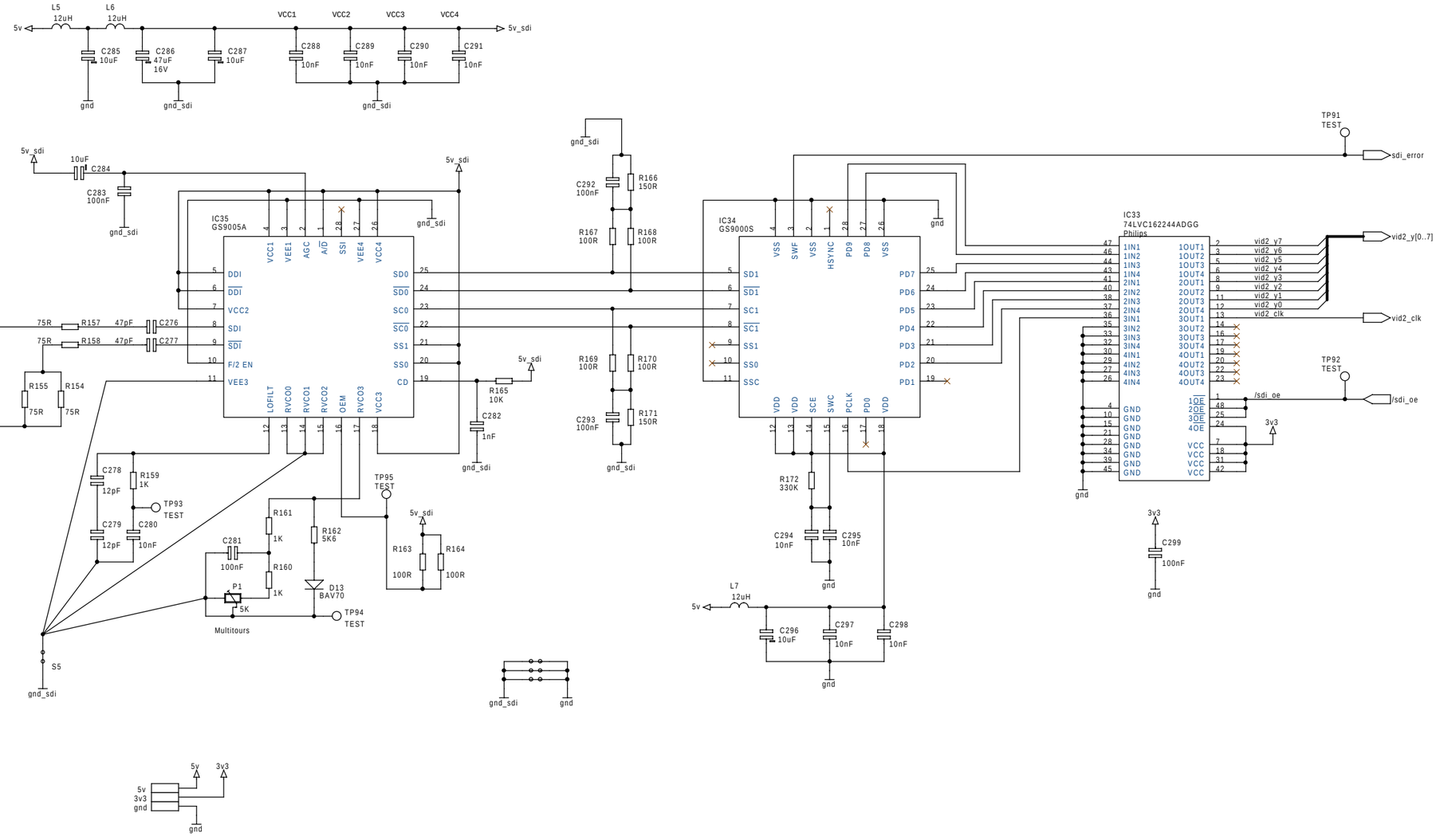
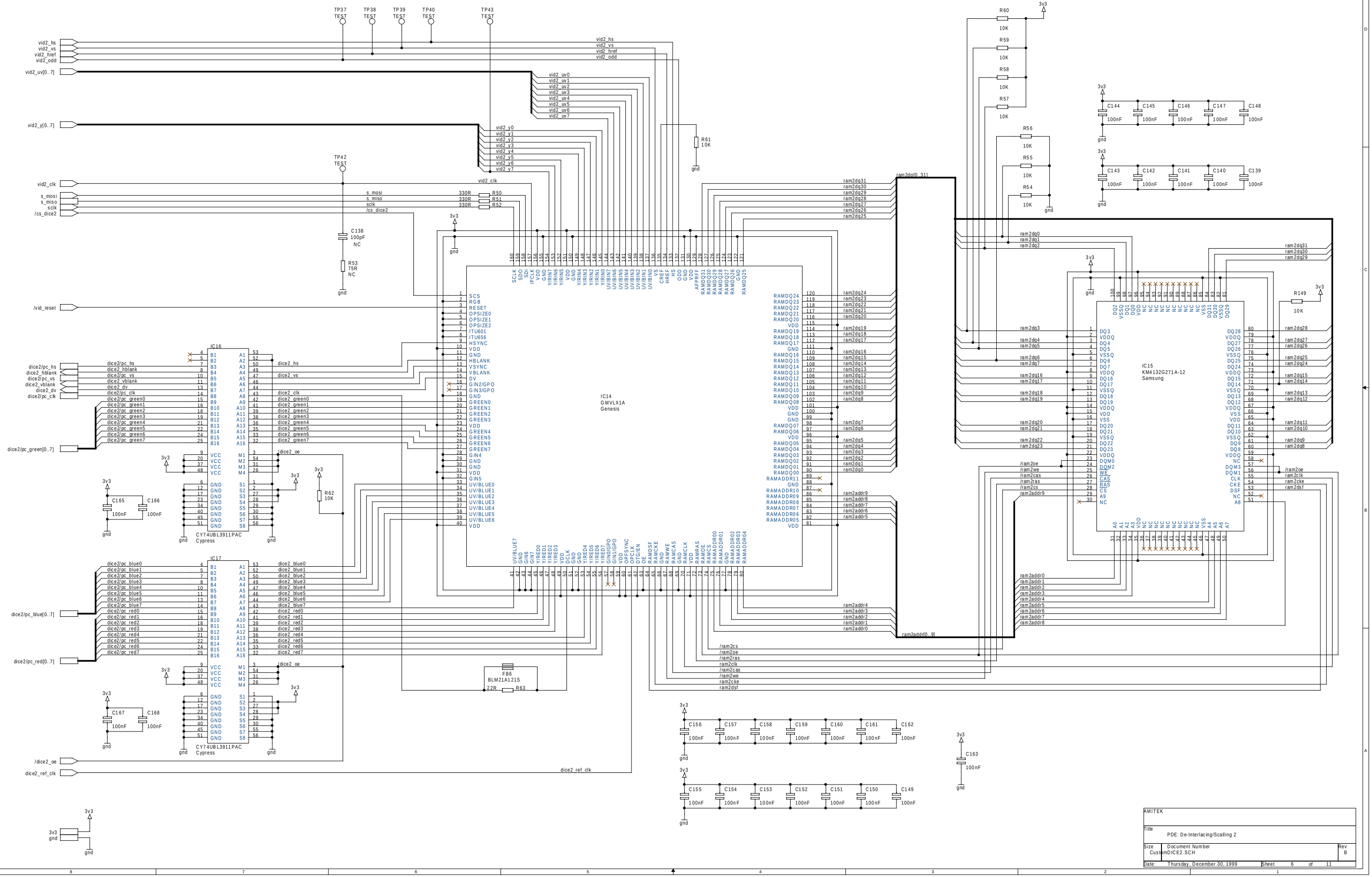
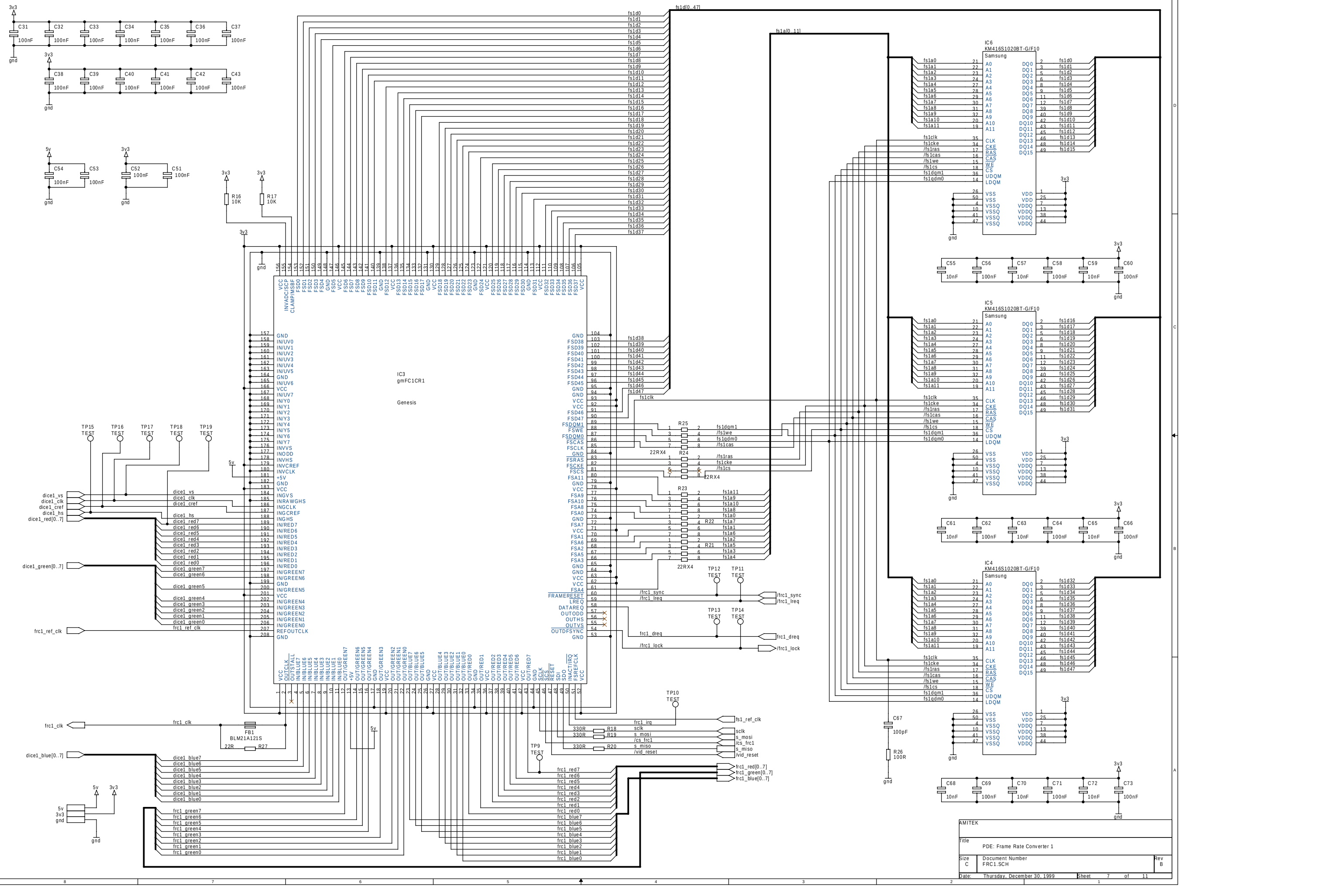
SDI input



AMITEK		
Title: PDE: SDI interface		
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TP37 TEST
TP38 TEST
TP39 TEST
TP40 TEST
TP43 TEST
TP42 TEST
vid2_hs
vid2_vs
vid2_href
vid2_odd
vid2_uv[0..7]
vid2_y[0..7]
vid2_clk
s_mosi
s_miso
sclk
/cs_dice2
/vid_reset
C138 100pF NC
R53 75R NC
R61 10K
330R R50
330R R51
330R R52
IC14 GMVLX1A Genesis
IC15 KM4132G271A-12 Samsung
R60 10K
R59 10K
R58 10K
R57 10K
R56 10K
R55 10K
R54 10K
R149 10K
C144 C145 C146 C147 C148 100nF
C143 C142 C141 C140 C139 100nF
ram2dq[0..31]
ram2addr[0..8]
IC16
IC17
CY74UBL3911PAC Cypress
dice2/pc_hs
dice2_hblank
dice2/pc_vs
dice2_vblank
dice2_dv
dice2/pc_clk
dice2/pc_green[0..7]
dice2/pc_blue[0..7]
dice2/pc_red[0..7]
/dice2_oe
dice2_ref_clk
C165 C166 C167 C168 100nF
FB6 BLM21A121S 22R R63
C156 C157 C158 C159 C160 C161 C162 C155 C154 C153 C152 C151 C150 C149 100nF
C163 100nF
/ram2cs
/ram2oe
/ram2ras
ram2clk
/ram2cas
/ram2we
ram2cke
ram2dsf
3v3
gnd
AMITEK
PDE: De-Interlacing/Scalling 2
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IC3
gmFC1CR1
Genesis
IC6
KM416S1020BT-G/F10
Samsung
IC5
KM416S1020BT-G/F10
Samsung
IC4
KM416S1020BT-G/F10
Samsung
fs1d[0..47]
fs1a[0..11]
frc1_ref_clk
dice1_vs
dice1_clk
dice1_cref
dice1_hs
dice1_red[0..7]
dice1_green[0..7]
dice1_blue[0..7]
frc1_clk
/frc1_sync
/frc1_lreq
/frc1_dreq
/frc1_lock
fs1_ref_clk
sclk
s_mosi
/cs_frc1
s_miso
/vid_reset
frc1_red[0..7]
frc1_green[0..7]
frc1_blue[0..7]
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Title: PDE: PLD

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IC22
EPF10K30AQC240-3
Altera

IC23
K4M16S1020BT-G/F10
Samsung

IC24
AMB01
AMITEK

J7
DEBUG

R93 100R
C242 100pF
R92 100R
C241 100pF
R94 0R
R90 330R
R91 330R
R95 1K
R96 1K
R97 1K
R98 1K
R81 1K
R80 1K
R79 1K
R78 1K
R82 0R
R83 0R
R84 0R
R85 0R
R86 330R
R87 330R
R88 330R
R89 330R
R144 22R
R145 22R
R99 4K7
R100 4K7
R101 4K7
R102 4K7
R103 4K7
R104 4K7
R105 4K7
R106 4K7
R107 4K7
R108 4K7
R109 4K7
R110 4K7
R111 4K7
R112 4K7
R113 4K7
R114 4K7
R115 4K7
R116 4K7
R117 4K7
R118 4K7
R119 4K7
R120 4K7
R121 4K7
R122 4K7
R123 4K7
R124 4K7
R125 4K7
R126 4K7
R127 4K7
R128 4K7
R129 4K7
R130 4K7
R131 4K7
R132 4K7
R133 4K7
R134 4K7
R135 4K7
R136 4K7
R137 4K7
R138 4K7
R139 4K7
R140 4K7
R141 4K7
R142 4K7
R143 4K7
C243 100nF
C244 100nF
C245 100nF
C246 100nF
C247 100nF
C248 100nF
C249 100nF
C250 100nF
C251 100nF
C252 100nF
C253 100nF
C254 100nF
C255 100nF
C256 100nF
C257 100nF
C258 100nF
C259 100nF
C260 100nF
C261 100nF
C262 100nF

ANALOG

ANA

CPU

CPU

MUX/GAMMA/OSD/HOST

PLD

DICE 1

DICE1

DICE 2

DICE2

FRAME RATE CONVERTER 1

FRC1

FRAME RATE CONVERTER 2

FRC2

CLOCK

CLOCK

SDI

PC ADC

PC_ADC

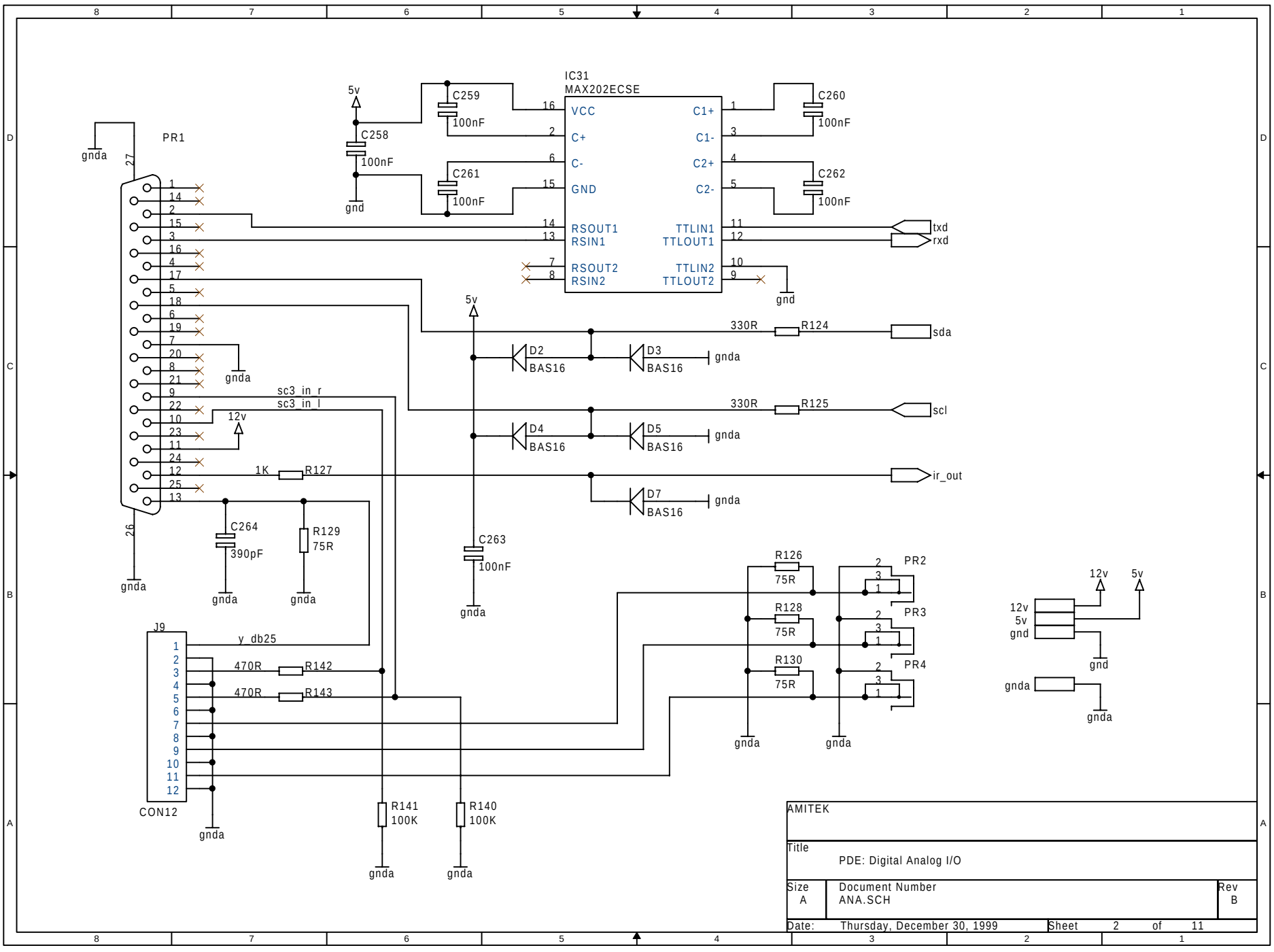
CON40

CON_PDP

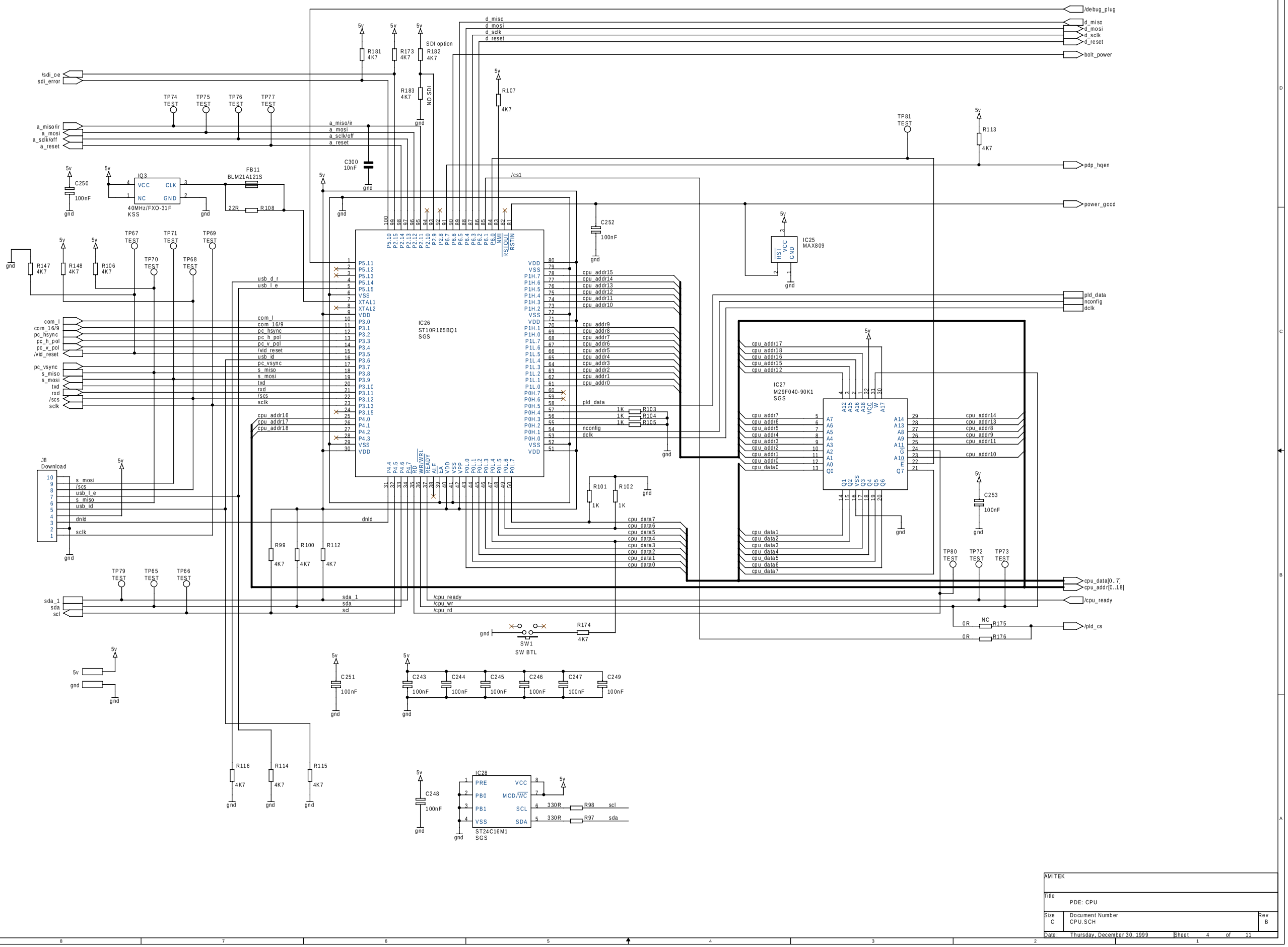
CON_DIS

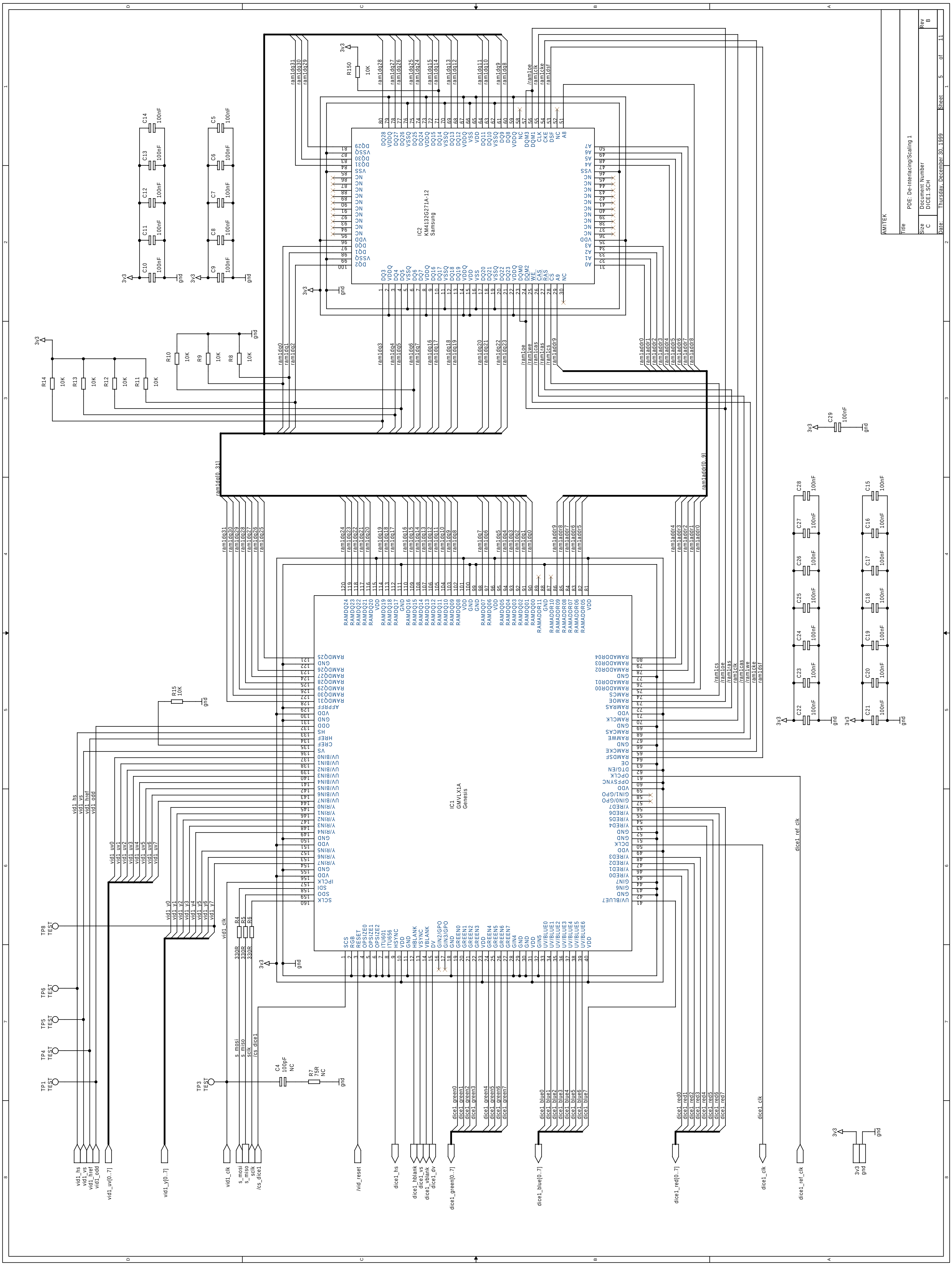
DB15-PC-F

Title	PDE top level
Size: D	Document Number: 208080B.SCH
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Title: PDE: Digital Analog I/O		
Size: A	Document Number: ANA.SCH	Rev: B
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/debug_plug
d_miso
d_mosi
d_sclk
d_reset
bolt_power
pdp_hqen
power_good
pld_data
nconfig
dclk
/sdi_oe
sdi_error
SDI option
NO SDI
a_miso/ir
a_mosi
a_sclk/off
a_reset
/cs1
IC26
ST10R165BQ1
SGS
IC27
M29F040-90K1
SGS
IC25
MAX809
IC28
ST24C16M1
SGS
40MHz/FXO-31F
KSS
BLM21A121S
SW BTL
J8
Download
usb_d_r
usb_l_e
com_l
com_16/9
pc_hsync
pc_h_pol
pc_v_pol
/vid_reset
usb_id
pc_vsync
s_miso
s_mosi
txd
rxd
/scs
sclk
dnld
cpu_addr16
cpu_addr17
cpu_addr18
cpu_addr15
cpu_addr14
cpu_addr13
cpu_addr12
cpu_addr11
cpu_addr10
cpu_addr9
cpu_addr8
cpu_addr7
cpu_addr6
cpu_addr5
cpu_addr4
cpu_addr3
cpu_addr2
cpu_addr1
cpu_addr0
cpu_data7
cpu_data6
cpu_data5
cpu_data4
cpu_data3
cpu_data2
cpu_data1
cpu_data0
cpu_data[0..7]
cpu_addr[0..18]
/cpu_ready
/cpu_wr
/cpu_rd
/pld_cs
sda_1
sda
scl
R181 4K7
R173 4K7
R182 4K7
R183 4K7
R107 4K7
R113 4K7
R147 4K7
R148 4K7
R106 4K7
R108 22R
R103 1K
R104 1K
R105 1K
R101 1K
R102 1K
R99 4K7
R100 4K7
R112 4K7
R116 4K7
R114 4K7
R115 4K7
R174 4K7
R175 0R
R176 0R
R98 330R
R97 330R
C300 10nF
C250 100nF
C252 100nF
C253 100nF
C251 100nF
C243 100nF
C244 100nF
C245 100nF
C246 100nF
C247 100nF
C249 100nF
C248 100nF
TP74 TEST
TP75 TEST
TP76 TEST
TP77 TEST
TP81 TEST
TP67 TEST
TP71 TEST
TP69 TEST
TP70 TEST
TP68 TEST
TP79 TEST
TP65 TEST
TP66 TEST
TP80 TEST
TP72 TEST
TP73 TEST
IQ3
VCC
CLK
NC
GND
5v
gnd
NC
AMITEK
Title
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Title
PDE: De-Interlacing/Scaling 1
Size
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IC1
GMVLX1A
Genesis
IC2
KM4132G271A-12
Samsung
ram1dq[0..31]
ram1addr[0..9]
vid1_hs
vid1_vs
vid1_href
vid1_odd
vid1_uv[0..7]
vid1_y[0..7]
vid1_clk
s_mosi
s_miso
sclk
/cs_dice1
/vid_reset
dice1_hs
dice1_hblank
dice1_vs
dice1_vblank
dice1_dv
dice1_green[0..7]
dice1_blue[0..7]
dice1_red[0..7]
dice1_clk
dice1_ref_clk
3v3
gnd